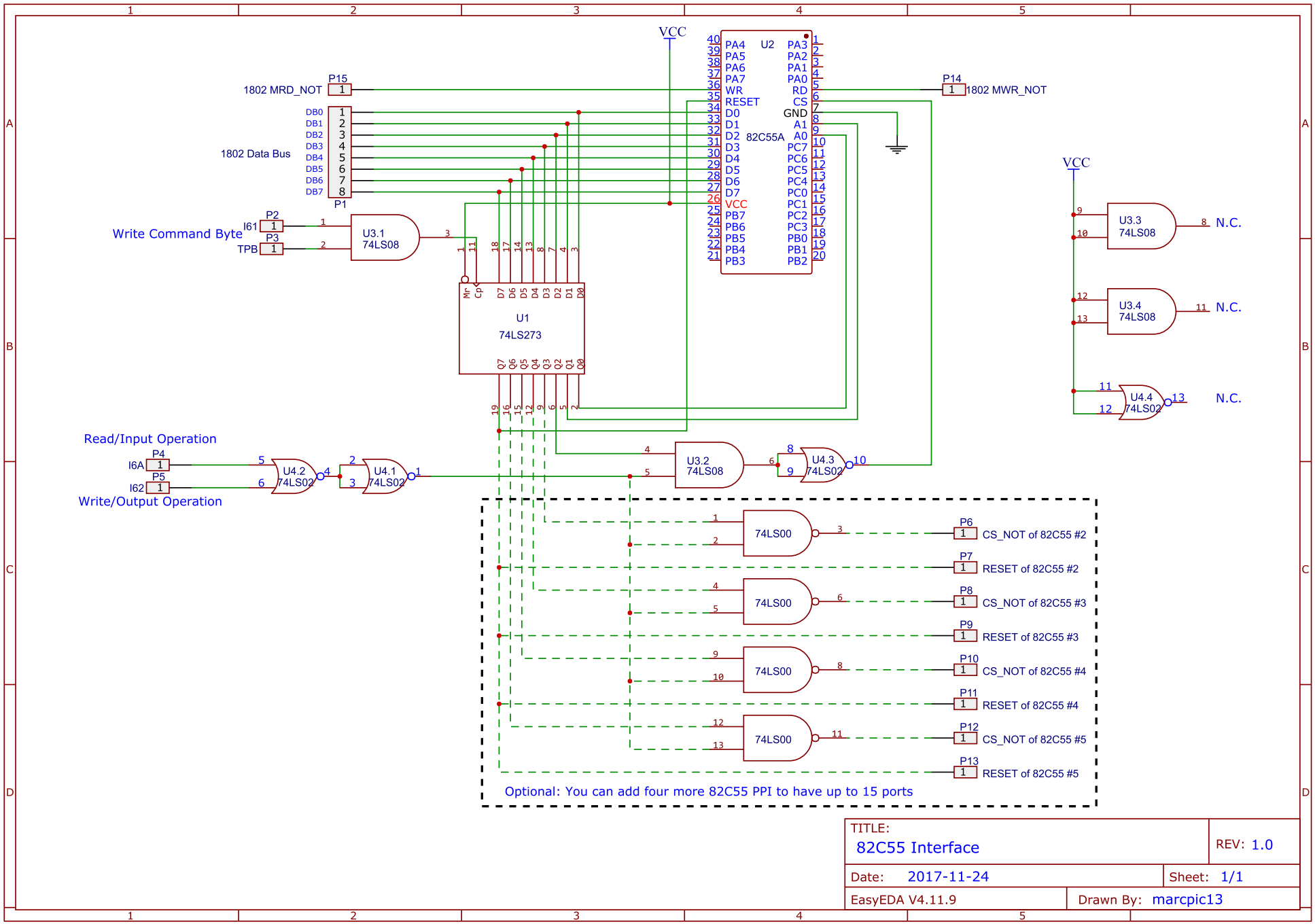
VCC
U2
82C55A
PA4
PA5
PA6
PA7
WR
RESET
D0
D1
D2
D3
D4
D5
D6
D7
VCC
PB7
PB6
PB5
PB4
PB3
PA3
PA2
PA1
PA0
RD
CS
GND
A1
A0
PC7
PC6
PC5
PC4
PC0
PC1
PC2
PC3
PB0
PB1
PB2
P15
1802 MRD_NOT
P14
1802 MWR_NOT
DB0
DB1
DB2
DB3
DB4
DB5
DB6
DB7
1802 Data Bus
P1
P2
I61
Write Command Byte
P3
TPB
U3.1
74LS08
U1
74LS273
Mr
Cp
D7
D6
D5
D4
D3
D2
D1
D0
Q7
Q6
Q5
Q4
Q3
Q2
Q1
Q0
U3.3
74LS08
N.C.
U3.4
74LS08
N.C.
U4.4
74LS02
N.C.
Read/Input Operation
P4
I6A
P5
I62
Write/Output Operation
U4.2
74LS02
U4.1
74LS02
U3.2
74LS08
U4.3
74LS02
74LS00
P6
CS_NOT of 82C55 #2
P7
RESET of 82C55 #2
P8
CS_NOT of 82C55 #3
P9
RESET of 82C55 #3
P10
CS_NOT of 82C55 #4
P11
RESET of 82C55 #4
P12
CS_NOT of 82C55 #5
P13
RESET of 82C55 #5
Optional: You can add four more 82C55 PPI to have up to 15 ports
TITLE:
82C55 Interface
REV: 1.0
Date: 2017-11-24
Sheet: 1/1
EasyEDA V4.11.9
Drawn By: marcpic13