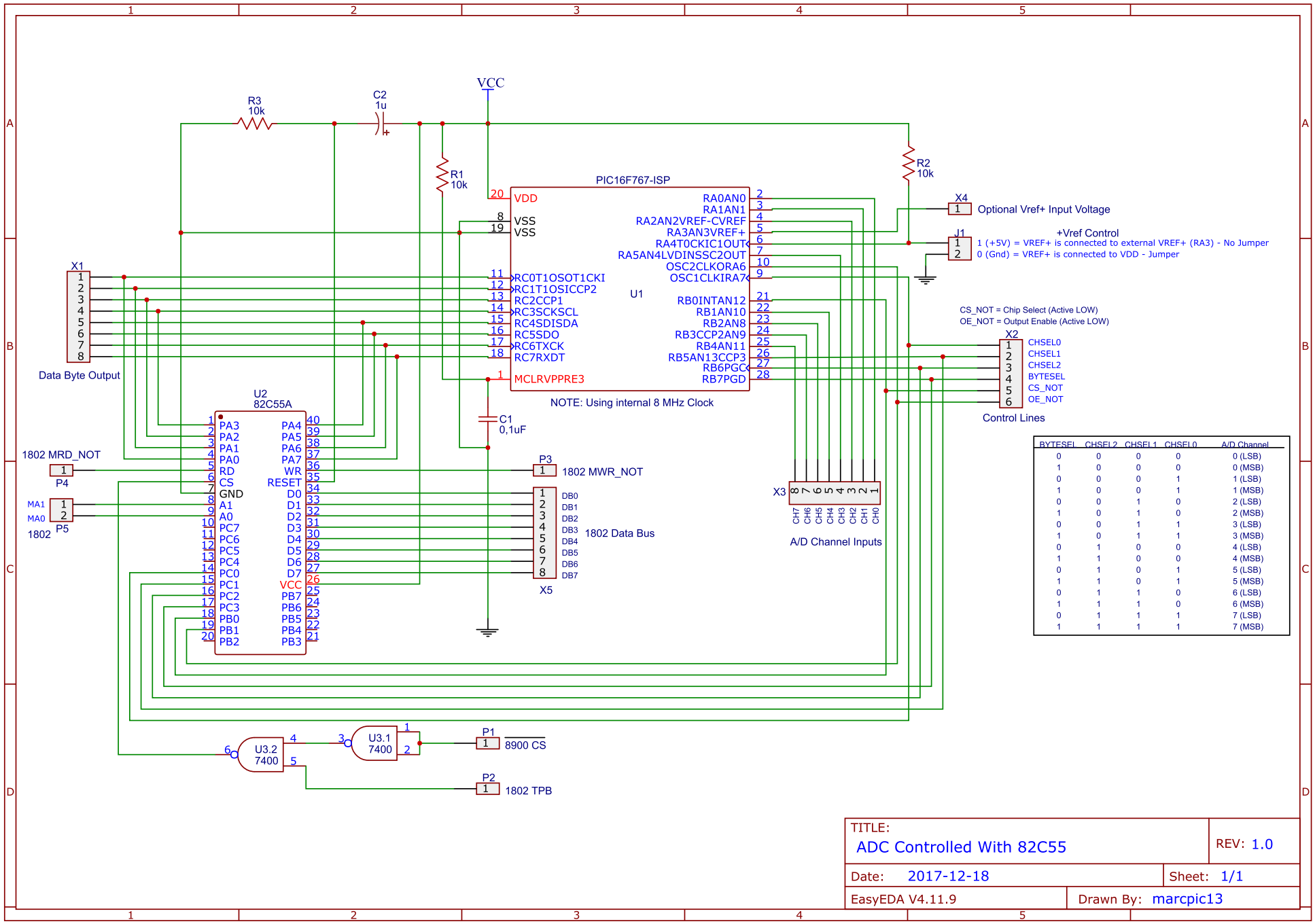

BYTESEL	CHSEL 2	CHSEL 1	CHSEL 0	A/D Channel
0	0	0	0	0 (LSB)
1	0	0	0	0 (MSB)
0	0	0	1	1 (LSB)
1	0	0	1	1 (MSB)
0	0	1	0	2 (LSB)
1	0	1	0	2 (MSB)
0	0	1	1	3 (LSB)
1	0	1	1	3 (MSB)
0	1	0	0	4 (LSB)
1	1	0	0	4 (MSB)
0	1	0	1	5 (LSB)
1	1	0	1	5 (MSB)
0	1	1	0	6 (LSB)
1	1	1	0	6 (MSB)
0	1	1	1	7 (LSB)
1	1	1	1	7 (MSB)

TITLE: ADC Controlled With 82C55		REV: 1.0
Date: 2017-12-18	Sheet: 1/1	
EasyEDA V4.11.9	Drawn By: marcpic13	